

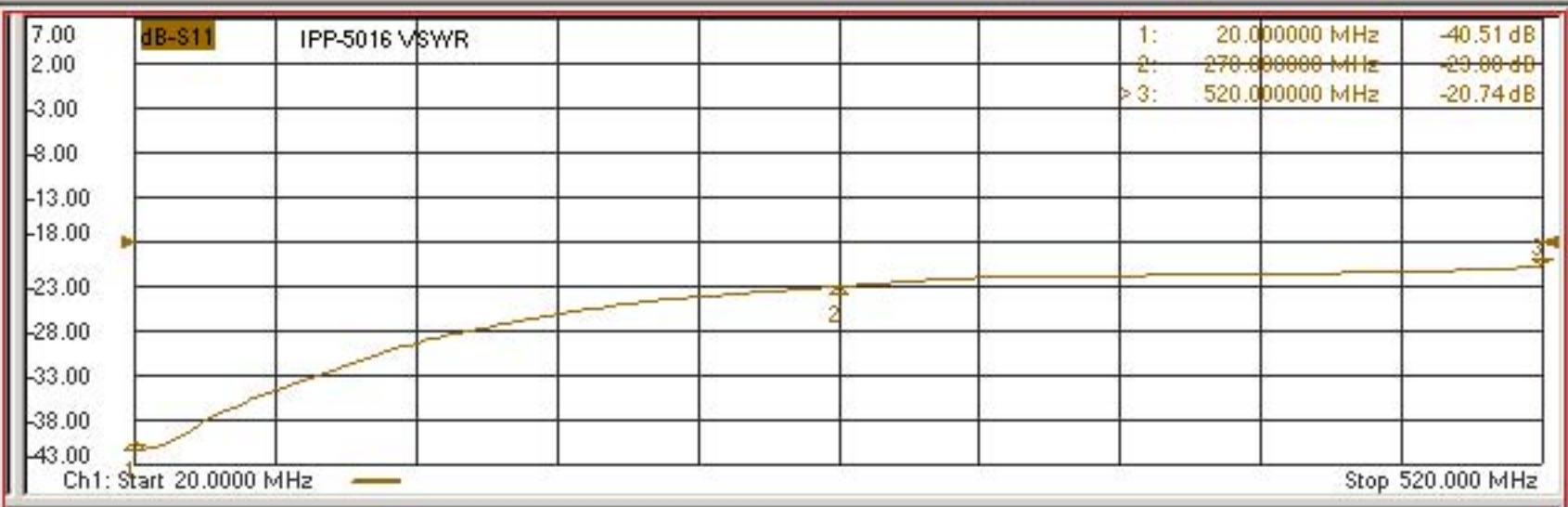
Math / Mem

Data>>Mem

Data / Mem

Data

Mem on/OFF



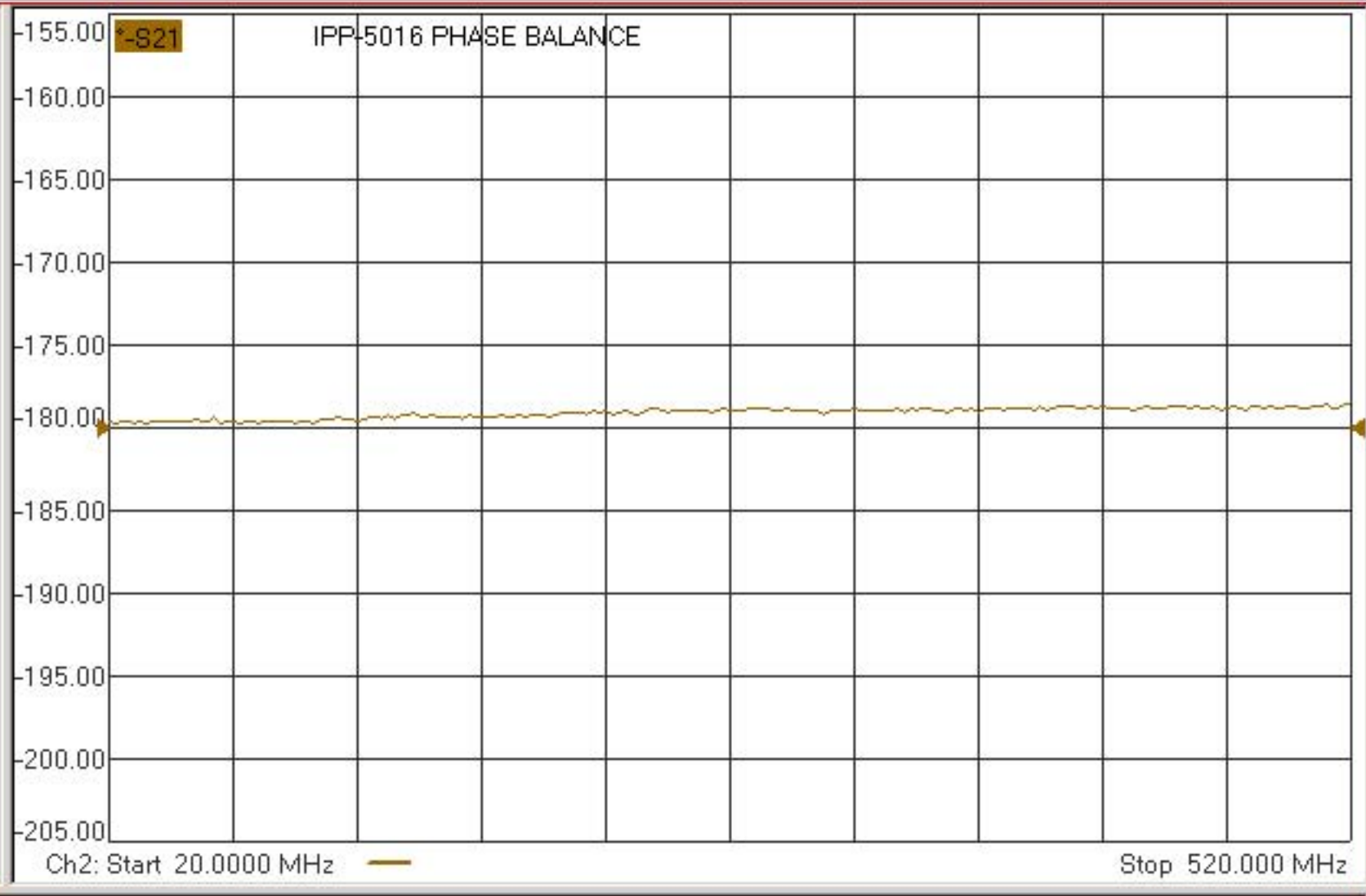
Math / Mem

Data>>Mem

Data / Mem

Data

Mem on/OFF



Ch2: Start 20.0000 MHz

Stop 520.0000 MHz

Status CH 2: S21 / M

C Response

LCL