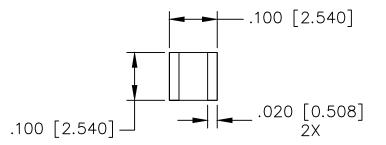
UNAUTHORIZED USE OF THIS DRAWING IS STRICTLY PROHIBITED

DAR	REV	DESCRIPTION	APPVD	DATE
9600	1	PRE-RELEASE	DG	2-24

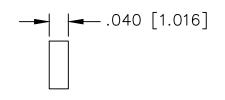
PRELIMINARY

DIMENSIONS AND SPECIFICATIONS ARE SUBJECT TO CHANGE UNTIL INITIAL RELEASE OF DRAWING.

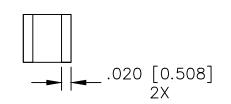
TOP SIDE



EDGE WRAP, 2X (OMIT FOR IPP-CCN1010-40N)



BOTTOM SIDE



NOTES:

- 1. SEE COOL CHIP APPLICATION NOTE FOR FURTHER INFORMATION.
- 2. PARTS ARE RoHS AND REACH COMPLIANT.
- 3. AVAILABLE IN TAPE AND REEL. SEE COOL CHIP APPLICATION NOTE FOR FURTHER INFORMATION.

ELECTRICAL SPECIFICATIONS

THERMAL CONDUCTIVITY: 2.3°C/W CAPACITANCE (SERIES): <0.1pF CAPACITANCE (SHUNT): <0.15pF

PART NO.	WRAPPING		
IPP-CCN1010-40	EDGE WRAPPED		
IPP-CCN1010-40N	NO EDGE WRAP		

MATERIAL SUBSTRATE: ALN	DWN S.Cornell	DATE 2-21-24	Power ®		
FINISH	ENG DG	DATE 2-24	Innovative Products		
GOLD OVER NICKEL	MANUF	DATE	COOL CHIP, .100 X .100		
TOLERANCE	CAGE COD	E: 437R2	·		
.XX ±.02 [.50] IN [MM] .XXX ±.010 [.254]	SHEET OF 1	SCALE 8:1	P/N IPP-CCN1010-40X REV. 1		